EE 320 1E Electronics I, Section 1

3 (2, 2) [Credit Hours (Lecture, Lab)]

COURSE SYLLABUS: Fall 2026

INSTRUCTOR INFORMATION

Instructor: Redha M. Radaydeh, PhD
Associate Professor Electrical Engineering
Department of Engineering and Technology

Office Location: AGET, Dept. of Engineering and Technology.
Office Hours: Tuesday & Thursday 11:30-12:30 pm, 1:45-2:30 pm or with appointment. Virtual meetings can be also scheduled.
Office Phone: 903-886-5471
University Email Address: Redha.Radaydeh@etamu.edu
Preferred Form of Communication: email.
Communication Response Time: within 24 hours (weekdays) to email.

COURSE INFORMATION

Class Meeting Schedule: Meets 8/20/2026 through 12/11/2026.
Class Meeting Dates: Weekly meetings; Wed 2:00 pm - 6:00 pm.
Classroom: AGET 215. Lectures will be given on campus. Labs will consist of software simulations and hardware in AGET 215 Lab.
Course Format: This course contains lectures and Lab sessions. The Lectures will be conducted on Wednesdays, which will be followed by Lab sessions.

Materials – Textbooks, Readings, Supplementary Readings

References:

- R. L. Boylestad and L. Nashelsky, Electronic devices and circuit theory, 11th Edition, Pearson Education, 2013. ISBN: 9780132622264
- S. Sedra and K. C. Smith, Microelectronic Circuits, 7th Edition, Oxford University Press, 2014. ISBN: 9780199339136

The *syllabus/schedule are subject to change.*

- D. A. Neamen, Electronic Circuit Analysis and Design, 4th Edition, McGraw-Hill, 2010. ISBN: 9780073380643

Laboratory Manual:

- R. L. Boylestad, L. Nashelsky, and F. J. Monssen, Lab Manual for Electronic Devices and Circuit Theory, 11th Edition, Pearson Education, 2013. ISBN: 9780132622455

Software Required:

- Microsoft Office - MS Word, Excel, PowerPoint
- Multisim (electronic circuits simulation program)

Course Description

This course is the first of two courses in the use of electronic devices in analog and digital circuits. The course covers characteristics of semiconductor devices; diodes, bipolar junction transistors (BJT), and field-effect transistors (FET). This course also covers diode applications, AC and DC analysis for BJT, models for electronic devices and circuits, analysis of diode, transistor, and FET basic circuits. The course has an associated Laboratory experiments set, which will require the use of simulation software (e.g. Multisim and PSpice) and hardware equipment

Prerequisites: EE 220 with a minimum grade of C.

Student Learning Outcomes:

1. Describe the fundamental principles and theories in electronic circuits.
2. Explain the basic structure and the mechanism of operation of diodes and transistors semiconductor devices.
3. Acquire knowledge on DC, AC, small-signal, and switching modes of operation of transistors.
4. Design, diagram, analyze and interpret experiments in electronic systems
5. Practice and validate equivalent circuit models of electronic devices for various applications.
6. Practice the basic principles of signal-stage amplifiers and switches using electronic devices.
7. Acquire Lab work experience on fundamentals concepts of semiconductor devices and their applications.
8. Operate modern software and numerical techniques, to enhance teamwork and communications

COURSE REQUIREMENTS

Minimal Technical Skills Needed

- Microsoft Office - MS Word, Excel, PowerPoint
- Multisim (electronic circuits simulation program) and/or Spice (simulation program for integrated circuits emphasis).

Instructional Methods

The instructional methods will include lectures, discussions, assignments, problem solving, Lab reports and exercises, and simulations using software. Instructions will be based on the course textbook and on the Lab manual. Course materials, announcements, and lecture notes will be posted on the course website.

Student Responsibilities or Tips for Success in the Course

Student must attend classes, participate in class work and discussions, and perform required course assessments supporting the anticipated learning objectives, such as Lab experimentations. Students are expected to regularly log into the course website to download course material, submit their course works as instructed, and follow up on new announcements. This course covers a more advanced content that requires at least 6 hours of extensive study per week.

" East *Texas A&M University acknowledges that there are legitimate uses of Artificial Intelligence, ChatBots, or other software that has the capacity to generate text, or suggest replacements for text beyond individual words, as determined by the instructor of the course.*

Any use of such software must be documented. Any undocumented use of such software constitutes an instance of academic dishonesty (plagiarism).

Individual instructors may disallow entirely the use of such software for individual assignments or for the entire course. Students should be aware of such requirements and follow their instructors 'guidelines. If no instructions are provided the student should assume that the use of such software is disallowed.

In any case, students are fully responsible for the content of any assignment they submit, regardless of whether they used an AI, in any way. This specifically includes cases in which the AI plagiarized another text or misrepresented sources.

13.99.99.R0.03 Undergraduate Academic Dishonesty

13.99.99.R0.10 Graduate Student Academic Dishonesty "

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Lab Safety Training
Students registered for this course must complete the required Lab safety training prior to entering the Lab and undertaking any activities. There are no exceptions to this University policy. Failure to complete the required training will preclude participation in Lab activities and assessments. Details are listed below:

As a new campus wide initiative to ensure compliance with system requirements, to improve efficiency and streamline processes, the Department of Environmental Health and Safety has created a required Student Laboratory Safety Training.

Student Laboratory Safety Training is included at the beginning of each identified course through our LMS system (D2L) in collaboration with Banner for the automatic assignment, tracking, communication and record of completion of all students enrolled in an identified lab and/or corequisite course. This course will be required even if D2L is not utilized for a particular course. The training has also been added to TrainTraq (2114178: General Laboratory Safety) for Instructors, professors, admins, department heads or any other faculty/staff to access so that they are aware of the contents of the training their students are taking.

This training covers general lab safety topics as follows:

- *Personal Protective Equipment (PPE)*
- *Globally Harmonized System (GHS)*
- *Safety Data Sheets (SDS)*
- *Hazard Communication (HazCom)*
- *Emergencies*
- *Physical Hazards*
- *Chemical Safety*
- *Spills*
- *Refrigerators / Freezers*
- *Glassware*
- *Compressed Gas Cylinder Safety*
- *Fume Hoods & BioSafety Cabinets*
- *Heat Stress*

In addition to the general training, any courses with specific hazards (i.e. machines, equipment, certain chemicals and/or processes) shall be covered by the instructor of that course. All instructors are responsible for ensuring students are aware and compliant with the deadlines of this requirement.

<u>Process for Students:</u>

- *When a student registers for a course that has been identified w/ the LST Compliance Attribute, they will be automatically enrolled in the D2L "Lab Safety Training" Course.*

- *Students will have access on the first-class day.*
- *Student, by default, has a Compliance Hold "OH" placed on their Banner Profile; this will be removed once student completes the training.*
- *Students will receive email notification prior to the start of term and then once per day for the first 5 days of the semester. The students will also receive notification through D2L's Pulse App via Intelligent Agents.*
- *Students that have not completed the training within the first 5 days of the start of term will begin getting an email notification on the 6th day indicating that their access to the courses associated to this training has been restricted.*
    - *Restriction and Access to course(s) will be handled via the custom web-application.*
- *Students will continue to get a daily email notification until the training is complete (Instructors will be copied so they are aware).*
- *On the 21st day of the term, students that have not completed the training will be notified via email that they have been dropped from the specific courses that required the "Lab Safety Training"*
- *On the 21st day of the term, a report will be generated, via the custom web-application, for EHS to review and to provide a written / formal request to the Registrar's Office to drop student(s) from the course(s) that they are enrolled in that have the associated LST Attribute.*
    - *A secondary method for obtaining the "drop list" would be to contact IR Office to assist w/ running a WebFocus report that would pull all students who still have a Compliance Hold Code of "OH".*

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GRADING

Final grades in this course will be based on the following scale:

A = 90%-100%

B = 80%-89%

C = 70%-79%

D = 60%-69%

F = 59% or Below

Weights of the assessments in the calculation of the final letter grade.

Assignments	15%
Class Participation	10%
Lab Work	30%
Midterm Exam	20%
Final Exam	25%
TOTAL	100%

The *syllabus/schedule are subject to change.*

Assessments

The following assessments will be performed during this course to assess individual progress towards learning outcomes:

Assessment	Weight	Due time
Assignments	15%	Weeks 3, 7, 10, 13
Midterm Exam	20%	Week 9
Class Participation	10%	Week 1 to 15
Final Exam	25%	Week 16
Lab Work	30%	Lab Reports

Relationship between Assessments and Course/Student Learning Outcomes

Learning Outcomes of Instruction	Assessment
1. Describe the fundamental principles and theories in electronic circuits.	Assignments
2. Explain the basic structure and the mechanism of operation of diodes and transistors semiconductor devices.	Assignments, Exams
3. Acquire knowledge on DC, AC, small signals, and switching modes of operation of transistors.	Lab Work
4. Design, diagram, analyze and interpret experiments in electronic systems	Exams, Lab Work
5. Practice and validate equivalent circuit models of electronic devices for various applications.	Assignments, Exams
6. Practice the basic principles of signal-stage amplifiers and switches using electronic devices.	Exams, Lab Work
7. Acquire Lab work experience on fundamentals concepts of semiconductor devices and their applications.	Lab Work
8. Operate modern software and numerical techniques, to enhance teamwork and communications	Assignments, Lab Work

Exams

The two exams will be closed book & closed notes. Relevant scientific formulas will be provided in the supplementary sheet. The use of personal phones is strictly prohibited during exams. Students will need to bring a scientific calculator for the exam. A makeup exam may be offered but an official permit for absence that fulfills University procedures should be available in a timely manner.

Assignments

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Solutions should be submitted on the due date. Students will need to submit one solution report per homework assignment. Solutions of an assignment will be due one week from the day it assigned. Unless prior arrangements are made with the instructor, no late submission of assignment solutions will be permitted. Some homework assignments may require the submission of simulation files created in Multisim. Any file that is flagged as infected with malware or viruses will not receive a grade.

Lab Work

Students will perform simulations and experiments of electronic circuits to study the main characteristics and practical applications of semiconductor electronic devices such as diodes and transistors. They will practice diode characteristic curves, rectifiers and regulated power supplies, regulators, clipping and clamping circuits, BJT characteristics and biasing, and analog amplifiers. The work will be supported by software simulation using Multisim and/or PSpice. There will be 6 Lab assignments distributed over the semester. Students will work in teams.

Additional guidelines for Lab work:

- Perform 6 extensive Lab experiments, each of which will require 2 weeks of work to be performed in each lab. Experiments are listed in the course outline below.
- Divide class into groups to do Lab work, with 3-4 students/group. Groups Lab management plan will follow the one shown below.
- Students are required to perform extensive simulations for each Lab experiment.
- Students will perform hardware work in the Lab.
- Each group will need to submit full report for each experiment.

Grading Policy for Lab Work:

Assessment	Weight	Due time
Lab Work & Reports	30%	Bi-Weekly Lab reports

Lab circuits must be built during Lab on an empty breadboard. They may not be built prior to the Lab period. Each student will be provided with Breadboard and tools and will be responsible for maintaining and returning the supplies at the end of the course. At the end of each Lab, you will be asked to demonstrate your functioning circuit to the instructor.
Formal Lab reports should follow the same approach used in the Lab, which is a Hypothesis/Test sequence. In Pre-Lab, you will be asked to design a circuit to perform a specific function. During the Lab time you will build the circuit and collect test data to show how the circuit performance. The report, then, should be constructed as follows:

1. **Cover page**: Your Lab report cover page should include the following information:
    - Course name and title

- Experiment number and title
- Names of team members and their IDs
- Instructor's name.
- Date

2. **Objectives**: State clearly the objectives of the experiment.
3. **Equipment required**: List all the equipment and components used in the experiments.
4. **Introduction:** Provide the necessary background to the problem that you are trying to solve in the Lab and the approach to solve it.
5. **Procedure:** Each part of the Lab experiment should explain the following**:**
    - Basic measurements and calculation
    - Explanation of the derived solution
    - Schematics developed that demonstrate the solution
6. **Results and Analysis**: Each part of Lab experiment should have the following:
    - Include tests used to prove the solution worked. One of the tests should be a Multisim simulation of your work. Include in your report a copy of the Multisim schematic.
    - Include a drawing of the solution you built in Lab.
    - Analyze the measured data and produce necessary plots by using graphing programs such as Excel and MATLAB.
    - State the observations made while performing the Lab and an explanation of your results.
7. **Conclusions:**
    - Describe what you did and learned from the Lab.
    - Explain at what degree the objectives of the Lab were achieved.
    - Describe possible real time applications from the work done in the Lab.

A Formal Lab report should enable someone else to duplicate your work and obtain the same results without reference to any other documents. This does not mean that you should append data sheets to your report but that the schematics and parts layout should be clear and accurate.

Submit the files containing the circuit simulation, a schematic, and data which explain the Lab results you obtained. Graphics must be created using a graphics program. Acceptable programs are Multisim, Visio, or similar graphics programs. Graphics in your Lab reports may not be hand-drawn.

Lab reports are due before Lab time one week after the Lab was performed. 10% per day will be deducted from the final Lab grade for each 24 hours or portion thereof that a Lab is late. Hardcopy of Lab reports are to be submitted to the instructor.

TECHNOLOGY REQUIREMENTS

LMS

All course sections offered by East Texas A&M University have a corresponding course shell in the myLeo Online Learning Management System (LMS). Below are technical requirements

LMS Requirements:
https://community.brightspace.com/s/article/Brightspace-Platform-Requirements

LMS Browser Support:
https://documentation.brightspace.com/EN/brightspace/requirements/all/browser_support.htm

YouSeeU Virtual Classroom Requirements:
https://support.youseeu.com/hc/en-us/articles/115007031107-Basic-System-Requirements

ACCESS AND NAVIGATION

You will need your campus-wide ID (CWID) and password to log into the course. If you do not know your CWID or have forgotten your password, contact the Center for IT Excellence (CITE) at 903.468.6000 or helpdesk@tamuc.edu**.**

Note: Personal computer and internet connection problems do not excuse the requirement to complete all course work in a timely and satisfactory manner. Each student needs to have a backup method to deal with these inevitable problems. These methods might include the availability of a backup PC at home or work, the temporary use of a computer at a friend's home, the local library, office service companies, Starbucks, ETAMU campus open computer lab, etc.

COMMUNICATION AND SUPPORT

If you have any questions or are having difficulties with the course material, please contact your instructor.

Technical Support

If you are having technical difficulty with any part of Brightspace, please contact Brightspace Technical Support at 1-877-325-7778. Other support options can be found here:

https://community.brightspace.com/support/s/contactsupport

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Interaction with Instructor Statement

Students are highly encouraged to participate in class activities, ask questions, and solve technical problems in class. They are also highly encouraged to work in groups during the Lab sessions, prepare full documentation of their Lab work, gain experience on software simulations and hardware work, and gain experience on team work, communication skills, and technical writing.

COURSE AND UNIVERSITY PROCEDURES/POLICIES

Course Specific Procedures/Policies

Syllabus Change Policy

The syllabus is a guide. Circumstances and events, such as student progress, may make it necessary for the instructor to modify the syllabus during the semester. Any changes made to the syllabus will be announced in advance.

University Specific Procedures

Student Conduct

All students enrolled at the University shall follow the tenets of common decency and acceptable behavior conducive to a positive learning environment. The Code of Student Conduct is described in detail in the Student Guidebook.
http://www.tamuc.edu/Admissions/oneStopShop/undergraduateAdmissions/studentGuidebook.aspx

Students should also consult the Rules of Netiquette for more information regarding how to interact with students in an online forum:
https://www.britannica.com/topic/netiquette

ETAMU Attendance

For more information about the attendance policy please visit the Attendance webpage and Procedure 13.99.99.R0.01.
http://www.tamuc.edu/admissions/registrar/generalInformation/attendance.aspx

http://www.tamuc.edu/aboutUs/policiesProceduresStandardsStatements/rulesProcedures/13students/academic/13.99.99.R0.01.pdf

Academic Integrity

Students at East Texas A&M University are expected to maintain high standards of integrity and honesty in all of their scholastic work. For more details and the definition of academic dishonesty see the following procedures:

Undergraduate Academic Dishonesty 13.99.99.R0.03
Undergraduate Student Academic Dishonesty Form

http://www.tamuc.edu/aboutUs/policiesProceduresStandardsStatements/rulesProcedures/documents/13.99.99.R0.03UndergraduateStudentAcademicDishonestyForm.pdf

Graduate Student Academic Dishonesty Form

http://www.tamuc.edu/academics/graduateschool/faculty/GraduateStudentAcademicDishonestyFormold.pdf

http://www.tamuc.edu/aboutUs/policiesProceduresStandardsStatements/rulesProcedures/13students/undergraduates/13.99.99.R0.03UndergraduateAcademicDishonesty.pdf

Students with Disabilities-- ADA Statement

The Americans with Disabilities Act (ADA) is a federal anti-discrimination statute that provides comprehensive civil rights protection for persons with disabilities. Among other things, this legislation requires that all students with disabilities be guaranteed a learning environment that provides reasonable accommodation of their disabilities. If you have a disability requiring an accommodation, please contact:

Office of Student Disability Resources and Services
East Texas A&M University
Velma K. Waters Library Rm 162
Phone (903) 886-5150 or (903) 886-5835
Fax (903) 468-8148
Email: studentdisabilityservices@tamuc.edu
Website: Office of Student Disability Resources and Services
http://www.tamuc.edu/campusLife/campusServices/studentDisabilityResourcesAndServices/

Nondiscrimination Notice

East Texas A&M University will comply in the classroom, and in online courses, with all federal and state laws prohibiting discrimination and related retaliation based on race, color, religion, sex, national origin, disability, age, genetic information or veteran status. Further, an environment free from discrimination based on sexual orientation, gender identity, or gender expression will be maintained.

Campus Concealed Carry Statement

The *syllabus/schedule are subject to change.*

Texas Senate Bill - 11 (Government Code 411.2031, et al.) authorizes the carrying of a concealed handgun in East Texas A&M University buildings only by persons who have been issued and are in possession of a Texas License to Carry a Handgun. Qualified law enforcement officers or those who are otherwise authorized to carry a concealed handgun in the State of Texas are also permitted to do so. Pursuant to Penal Code (PC) 46.035 and East Texas A&M Rule 34.06.02.R1, license holders may not carry a concealed handgun in restricted locations.

For a list of locations, please refer to the Carrying Concealed Handguns On Campus document and/or consult your event organizer.

Web url:
http://www.tamuc.edu/aboutUs/policiesProceduresStandardsStatements/rulesProcedures/34SafetyOfEmployeesAndStudents/34.06.02.R1.pdf

Pursuant to PC 46.035, the open carrying of handguns is prohibited on all East Texas A&M campuses. Report violations to the University Police Department at 903-886-5868 or 9-1-1.

Teas Texas A&M Supports Students' Mental Health

The Counseling Center at East Texas A&M, located in the Halladay Building, Room 203, offers counseling services, educational programming, and connection to community resources for students. Students have 24/7 access to the Counseling Center's crisis assessment services by calling 903-886-5145. For more information regarding Counseling Center events and confidential services, please visit **www.tamuc.edu/counsel**

Department or Accrediting Agency Required Content

The Electrical Engineering program is in the process to obtain ABET accreditation. The course material, its expected deliverables, grading policy, organization, and expected learning outcomes are designed to meet the ABET requirements.

COURSE OUTLINE / CALENDAR

The instructor reserves the right to adjust the schedule to meet the needs of the class, and any changes will be communicated in a timely manner.

Course schedule: The sequence of chapters follows the textbook.

Week	Topic	Chapter
1-2	Semiconductor Diodes	1
	Assignment 1	
3-6	Diode Applications	2
4	Assignment 2	
7-8	Bipolar Junction Transistors (BJTs)	3
8	Assignment 3	
9-11	DC Biasing—BJTs	4
9	Midterm Exam	
12-14	BJT AC Analysis	5
12	Assignment 4	
15	Field-Effect Transistors (FETs) – as time permits	6
16	Final Exam	

Lab schedule: The sequence of experiments follows the Lab manual.

Exp. No.	List of Lab Works	Week
1	Semiconductor diodes: Diode Characteristic Curves: Semiconductor diode, LED and Zener	3-4
2	Diode applications (Part I): Diode Rectifiers and regulated power supply	5-6
3	Diode applications (Part II): Zener Diode Regulation and Design	7-8
4	Bipolar junction transistors (BJT): BJT Characteristic Curves	9-10
5	BJT Biasing: Fixed-Bias, Voltage Divider Bias, operating point stability	11-12
6	Transistor Analog Applications: Common-emitter transistor Amplifiers	13-14

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